



15/17  
Pat 4/8/03

Docket No.: M4065.0842/P842  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Roger Panicacci, et al

Confirmation No.: 3217

Application No.: 09/264,501

Group Art Unit: 2697

Filed: March 8, 1999

Examiner: D. Wu

For: READOUT CIRCUIT WITH GAIN AND  
ANALOG-TO-DIGITAL CONVERSION  
FOR IMAGE SENSOR

04/07/2003 DTESSEM1 00000037 09264501

01 FC:1201

166.00 OP

AMENDMENT

Box Non-Fee Amendment  
Commissioner for Patents  
Washington, DC 20231

Dear Sir:

In response to the Office Action dated January 2, 2003 (Paper No. 10), please amend the above-identified U.S. patent application as follows:

IN THE SPECIFICATION

Please rewrite the paragraph at page 12, lines 9-21 as follows:

AI cont  
The circuit 52 further includes an analog multiplexer (MUX) 64 which allows the differential value corresponding to a selected one of the pixels to be passed through to a charge transimpedance amplifier (CTIA) 66 which can have a variable gain. The amplified differential signal is stored by another sample-and-hold circuit 68 and is provided to a summing node 70. The summing node 70 also receives signal from a calibration network 72 which uses reference voltages VREF2, VREF3. The output of the summing node 70 is provided to an analog-to-digital converter (ADC) 74 which uses a reference voltage